



Appl. No. 10/050,347

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. ....10/050,347  
Filing Date .....January 15, 2002  
Inventor..... Gurtej S. Sandhu et al.  
Assignee .....Micron Technology, Inc.  
Group Art Unit.....2813  
Examiner .....Schillinger, Laura M.  
Attorney's Docket No. ....MI22-1897  
Customer No. ....021567  
Title: Methods of Forming Transistors

**RESPONSE TO JUNE 9, 2005 NOTICE OF ALLOWANCE**  
**TO ACCOMPANY RCE FILING**

To: Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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**AMENDMENTS**

**Introductory Comments**

In reply to the Notice of Allowance of June 9, 2005, applicant amends and remarks  
as follows.